



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Inventor Application of:
Keisuke GOTO, et al.

Appl. No.: 09/826,179

Filed: April 4, 2001

For: DLL CIRCUIT AND METHOD GENERATING
TIMING SIGNALS

Art Unit: 2816

Examiner: LE, Dinh Thanh

Atty Docket: KPM-01501

#9/Amend A
Andrew
3/27/03

CERTIFICATE OF MAILING

I hereby certify that the foregoing document is being deposited with the United States Postal Service as first class mail, postage prepaid, "Post Office to Addressee", in an envelope addressed to: Commissioner of Patents, Washington, DC 20231 on March 13, 2003.

Tracey A. Newell
Tracey Newell

AMENDMENT AND RESPONSE

Commissioner for Patents
Washington, D.C. 20231

Sir:

This paper is being provided in response to the Office Action dated September 16, 2002, for the above-captioned U.S. patent application.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those which may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required for consideration of this paper (including fees for net addition of claims) are authorized to be charged in two originally-executed copies of an Amendment Transmittal Letter filed herewith.

Attached is a clean-copy list of all pending claims as amended herein.

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